



K 系列

K Series

特点 Features

- 高性能电源滤波器
High performance filters
- 具有良好的共模和差模干扰抑制特性
Good performance of common mode and differential mode interference suppression

应用 Applications

- 用于大多数数字设备(尤其是开关电源)用来抑制连续或间隙性脉冲干扰。
K Series are available for digital systems(particularly for switching power supplies) to suppress the continuity or intermittent pulse interference.

技术参数 Technical Data

额定电压 Rated Voltage	120/250VAC
工作频率 Operating Frequency	50/60Hz
额定电流 Rated Current	3A-100A
耐压试验:(一分钟) Test Voltage (1min)	1500VDC (line/line) 1500VAC (line/ground)
气候类别 Climatic Category	25/085/21

型号 Model	端子方式 Terminal		额定电流 Rated Current (A)	接地电容量 Ground Capacitance (nF)	泄漏电流 Leakage Current ≤(mA)	放电电阻 Discharge Resistance (MΩ)	电路图 Circuit Diagram fig.	外形尺寸 Dimension fig.
	入 Input	出 Output						
* DL-3K1			3	9.4	1.5	1.5	1	1
* DL-6K1			6	9.4	1.5	1.5	1	1
* DL-10K1			10	9.4	1.5	1.5	1	1
DL-12K1			12	9.4	1.5	1.0	2	2
DL-15K1			15	13.3	1.5	1.5	1	5
* DL-20K3			20	9.4	1.5	1.5	1	3
* DL-20K32			20	6.6	1.2	1.5	1	3
DL-30K3			30	9.4	1.5	1.5	1	3
DL-40K1			40	9.4	1.5	1.0	2	8
DL-40K3			40	9.4	1.5	1.5	1	4
DL-50K3			50	9.4	1.5	1.5	1	6
DL-80K11			80	40	5	0.68	2	7
DL-100K1			100	40	5	0.68	2	7

* : VDE APPROVAL



电原理图 Typical Circuit Diagram

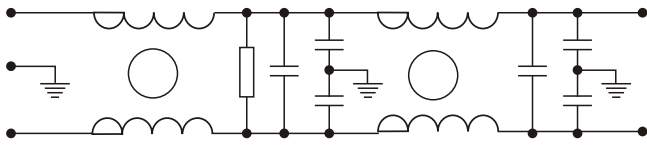


fig.1

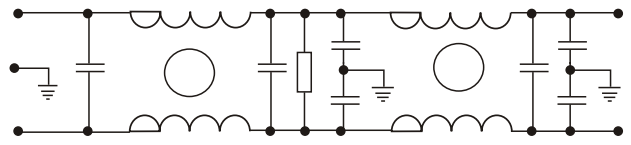


fig.2

外形尺寸 Outline Drawing and Dimensions (mm)

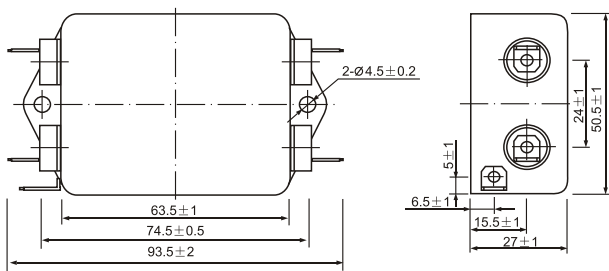


fig.1

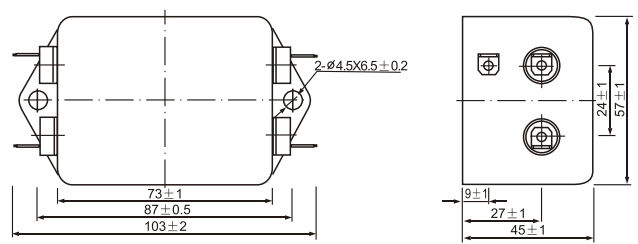


fig.2

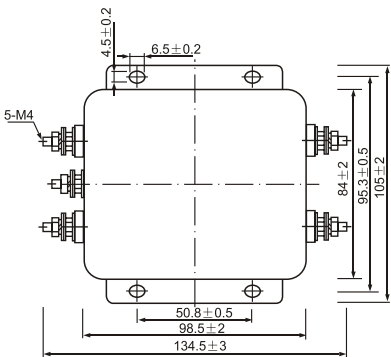


fig.3

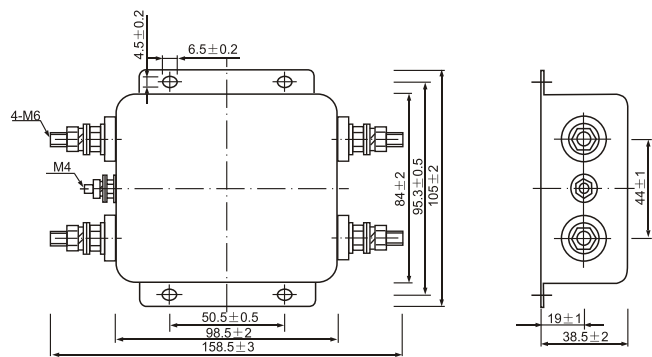


fig.4

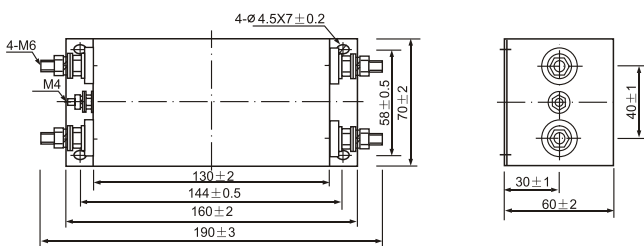


fig.5

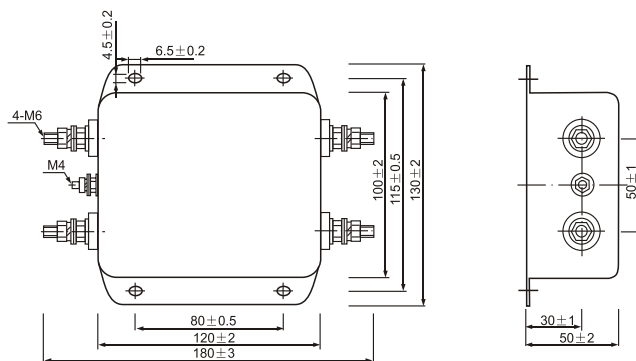


fig.6



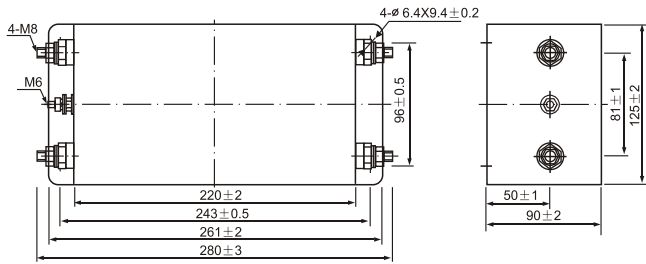


fig.7

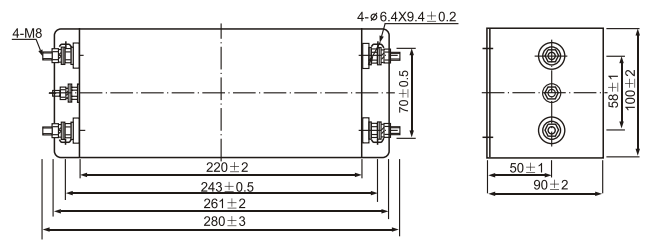
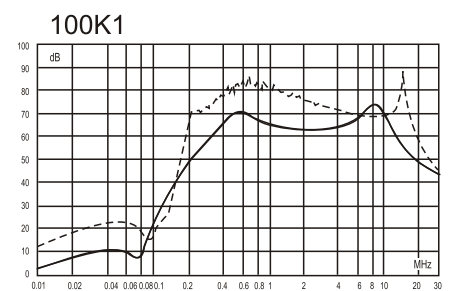
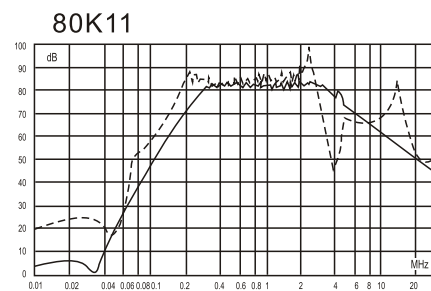
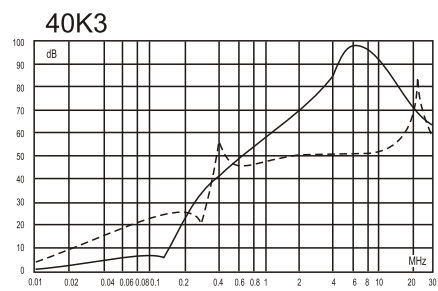
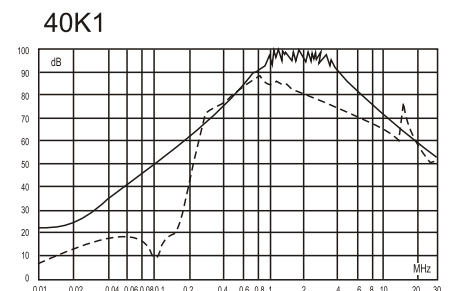
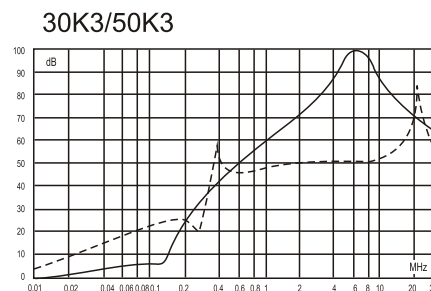
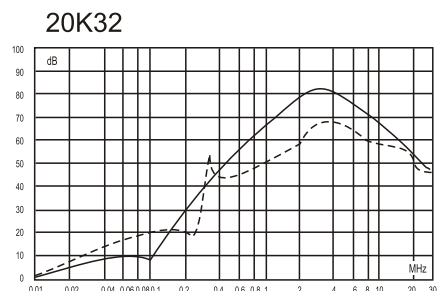
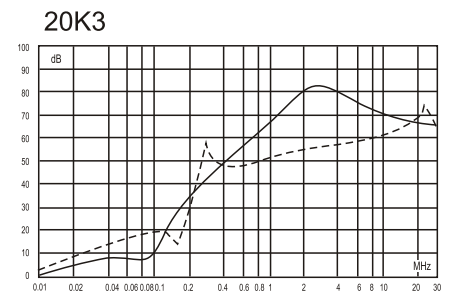
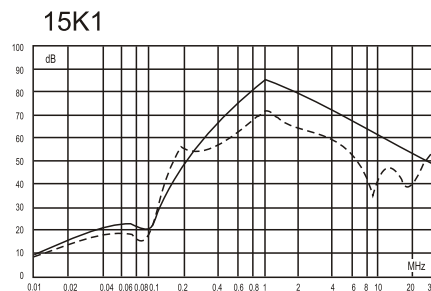
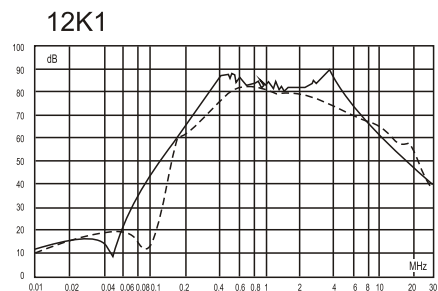
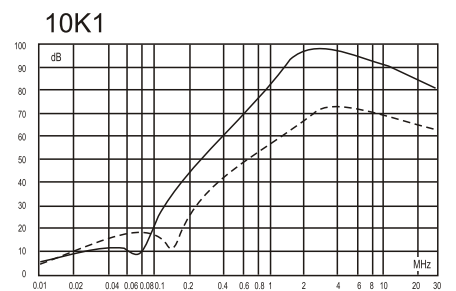
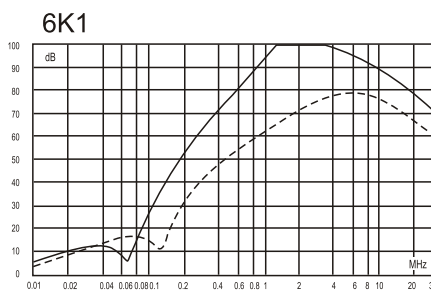
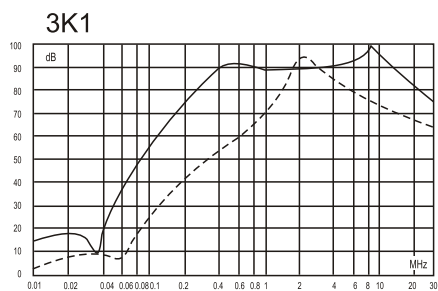


fig.8

插入损耗 Insertion Loss in dB

(50 Ω 测量系统, IEC/CISPR No. 17 号出版物 Measured in 50 Ω system, as IEC/CISPR No. 17)



—— 共模(Common Mode)
 - - - - 差模(Differential Mode)

